

## U.S. UTILITY Patent Application

**O.L.P.E.**

PATENT DATE

CCANNED



22

APPLICATION NO.

**CONT/PRIOR**

**CLASS**

SUBCLASS

ART UNIT

**EXAMINER**

09/939417

326 257

331.

~~2819~~ 282

Lewis

## APPLICANTS

Leonard Forbes

## Title

Vertical transistor with horizontal gate layers

PTO-2040  
12/99

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CC-3 15-77-100

*[Handwritten signature]*

(Primary Examiner)

(Data)

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(to) \_\_\_\_\_ have been obtained

ISSUE BATCH NUMBER

(Legal Instruments Division)

(10)

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